

TENTATIVE

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12.1" SVGA

TECHNICAL SPECIFICATION

AA121SL03

mitsubishi
MITSUBISHI ELECTRIC Corp.

Date: May.18,'04

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1. APPLICATION

This specification applies to color TFT-LCD module, AA121SL03.

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(3) Specific Usage

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2. OVERVIEW

AA121SL03 is 12.1" color TFT-LCD (Thin Film Transistor Liquid Crystal Display) module composed of LCD panel, driver ICs, control circuit, and backlight unit.

By applying 6 bit or 8 bit digital data, 800 × 600, 260 K-color or 16.1 M-color images are displayed on the 12.1" diagonal screen. Input power voltage is 3.3V for LCD driving.

Inverter for backlight is not included in this module. General specifications are summarized in the following table:

ITEM	SPECIFICATION
Display Area (mm)	246.0 × 184.5 (12.106-inch diagonal)
Number of Dots	800 × 3 (H) × 600 (V)
Pixel Pitch (mm)	0.3075 (H) × 0.3075 (V)
Color Pixel Arrangement	RGB vertical stripe
Display Mode	Normally white
Number of Color	260 K(6 bit/color), 16.1 M(8 bit/color)
Electrical Interface	LVDS(6 bit/8 bit)
Optimum Viewing Angle(Contrast ratio)	6 o'clock
Brightness (cd/m ²)	350
Module Size (mm)	280.0 (W) × 210.0 (H) × 12.0 (D)
Module Mass (g)	720
Backlight Unit	CCFL, 2-tubes, edge-light, replaceable
Surface Treatment	Antiglare and hard-coating 3H

Characteristic value without any note is typical value.

3. ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	MIN.	MAX	UNIT
Power Supply Voltage for LCD	VCC	0	4.0	V
Logic Input Voltage	VI	0	VCC+0.3	V
Lamp Voltage	VL	0	2000	Vrms
Lamp Current	IL	0	18	mArms
Lamp Frequency	FL	--	80	kHz
Operation Temperature(Panel) <small>Note 1,2)</small>	T _{op} (Panel)	0	60	°C
Operation Temperature(Ambient) <small>Note 2)</small>	T _{op} (Ambient)	0	60	°C
Storage Temperature <small>Note 2)</small>	T _{stg}	-20	70	°C

[Note]

1) Measured at the center of active area and at the center of panel back surface

2) Top, Tstg ≤ 40°C : 90%RH max. without condensation

Top, Tstg > 40°C : Absolute humidity shall be less than the value of 90%RH at 40°C without condensation.

4. ELECTRICAL CHARACTERISTICS

(1) TFT-LCD

Ambient temperature: Ta = 25

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	Remarks
Power Supply Voltages for LCD	VCC	3.0	3.3	3.6	V	A)
Power Supply Currents for LCD	ICC	--	300	450	mA	VCC = 3.3 V B)
Permissible Input Ripple Voltage	VRP	--	--	100	mVp-p	VCC = 3.3V
Logic Input Voltage	High	VIH	2.0	--	VCC+0.3	MODE, SC (See P8)
	Low	VIL	0	--	0.3	

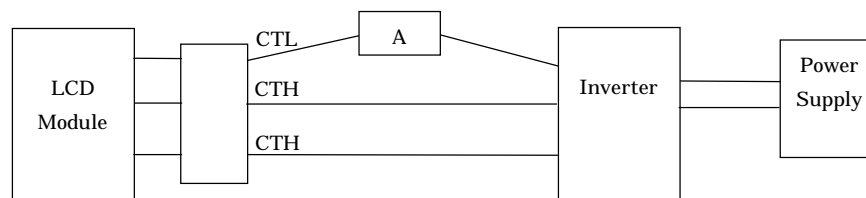
(2) Backlight

Ta = 25

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	Remarks
Lamp Voltage	VL	--	540	--	Vrms	IL = 12.0 mArms
Lamp Current	IL	6.0	12.0	13.0	mArms	Note 1), 5)
Lamp Frequency	FL	30	--	60	kHz	Note 2)
Starting Lamp Voltage	VS	1000	--	--	Vrms	Ta = 25°C Note 3)
		1200	--	--		Ta = 0°C Note 3)
Lamp Life Time	LT	50,000	--	--	h	IL = 12.0 mArms, Continuous operation Note 3),4)

[Note]

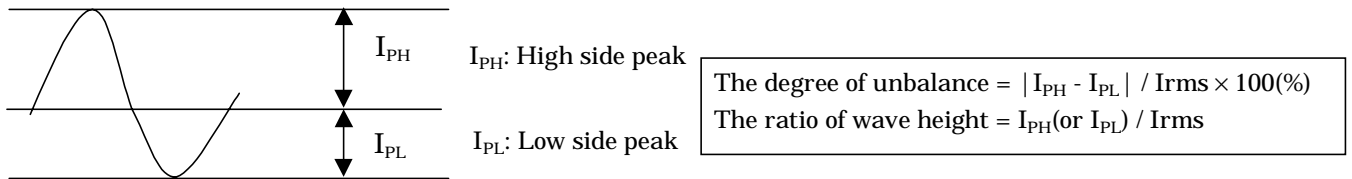
1) Lamp Current measurement method (The current meter is inserted in low voltage line.)



- 2) Lamp frequency of inverter may produce interference with horizontal synchronous frequency, and this may cause horizontal beat on the display. Therefore, please adjust lamp frequency, and keep inverter as far from module as possible or use electronic shielding between inverter and module to avoid the interference.
- 3) Lamp life time is defined as the time either when the brightness becomes 50% of the initial value, or when the starting lamp voltage does not meet the value specified in this table.
- 4) The life time of the backlight depends on the ambient temperature. The life time will decrease under low/high temperature.
- 5) Please use the inverter which has symmetrical current wave form as follows,

The degree of unbalance: less than 10%

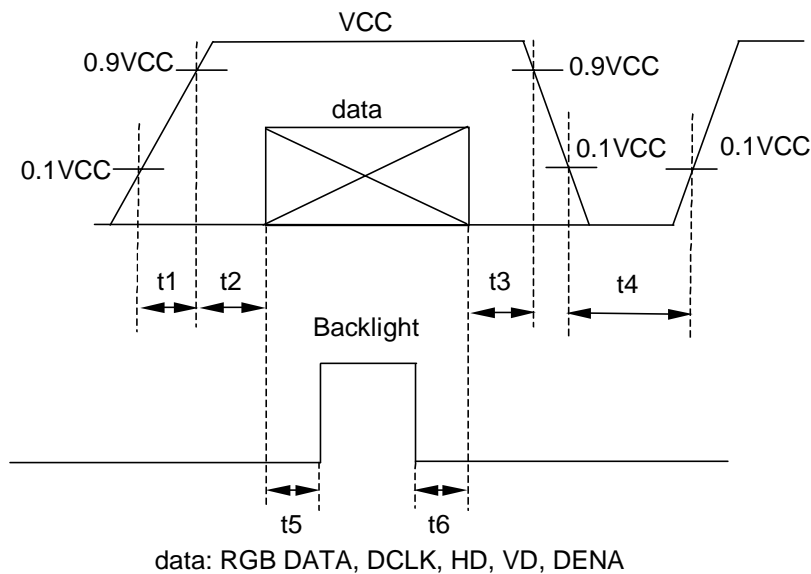
The ratio of wave height: less than $\sqrt{2} \pm 10\%$



CURRENT WAVE FORM

A) Power and signals sequence:

- | | |
|-----------------------------|--------------------------|
| $t1 \leq 10 \text{ ms}$ | $400 \text{ ms} \leq t4$ |
| $0 < t2 \leq 50 \text{ ms}$ | $200 \text{ ms} \leq t5$ |
| $0 < t3 \leq 50 \text{ ms}$ | $0 \leq t6$ |

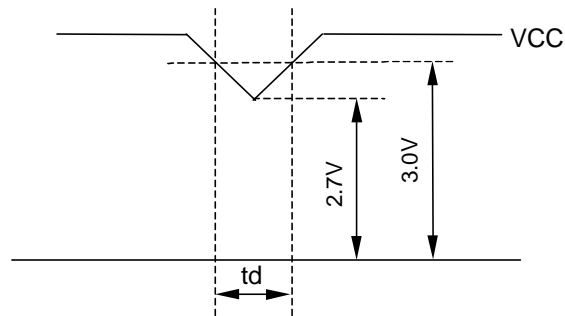


VCC-dip conditions:

1) When $2.7\text{ V} \leq VCC < 3.0\text{ V}$, $t_d \leq 10\text{ ms}$

2) When $VCC < 2.7\text{ V}$

VCC-dip conditions should also follow the power and signals sequence.



B) Typical current condition:

64-gray-bar pattern

600 line mode

$VCC = +3.3\text{ V}$, $f_H=37.9\text{kHz}$, $f_V=60.3\text{Hz}$, $f_{CLK}= 40\text{MHz}$

5. INTERFACE PIN CONNECTION

(1) CN 1(Interface Signal)

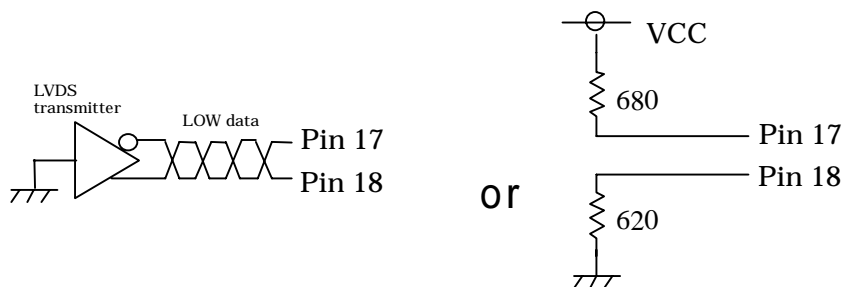
Used connector: FI-SEB20P-HF

Corresponding connector: FI-S20S[for discrete Wire], FI-SE20M[for SMT]

Pin No.	Symbol	Function(ISP 6 bit compatibility mode)		Function(ISP 8 bit compatibility mode)
		6 bit input	8 bit input	
1	VCC	+3.3 V Power supply		←
2	VCC	+3.3 V Power supply		←
3	GND	GND		←
4	GND	GND		←
5	Link 0-	R0, R1, R2, R3, R4, R5, G0	R2, R3, R4, R5, R6, R7, G2	R0, R1, R2, R3, R4, R5, G0
6	Link 0+	R0, R1, R2, R3, R4, R5, G0	R2, R3, R4, R5, R6, R7, G2	R0, R1, R2, R3, R4, R5, G0
7	GND	GND		←
8	Link 1-	G1, G2, G3, G4, G5, B0, B1	G3, G4, G5, G6, G7, B2, B3	G1, G2, G3, G4, G5, B0, B1
9	Link 1+	G1, G2, G3, G4, G5, B0, B1	G3, G4, G5, G6, G7, B2, B3	G1, G2, G3, G4, G5, B0, B1
10	GND	GND		←
11	Link 2-	B2, B3, B4, B5, HD, VD, DENA	B4, B5, B6, B7, HD, VD, DENA	B2, B3, B4, B5, HD, VD, DENA
12	Link 2+	B2, B3, B4, B5, HD, VD, DENA	B4, B5, B6, B7, HD, VD, DENA	B2, B3, B4, B5, HD, VD, DENA
13	GND	GND		←
14	CLKIN-	Clock -		←
15	CLKIN+	Clock +		←
16	GND	GND		←
17	Link3-	See: *2)	R0, R1, G0, G1, B0, B1	R6, R7, G6, G7, B6, B7
18	Link3+	See: *2)	R0, R1, G0, G1, B0, B1	R6, R7, G6, G7, B6, B7
19	MODE	L=ISP 6 bit compatibility mode		H=ISP 8 bit compatibility mode
20	SC	Reverse scan control. (L : Normal , H : Reverse)		←

*1) The shielding case is connected with GND

*2) Recommended wiring of Pin 17,18 (6 bit input)



(2) CN 2(Backlight)

Backlight-side connector: BHR-04VS-1 (JST)

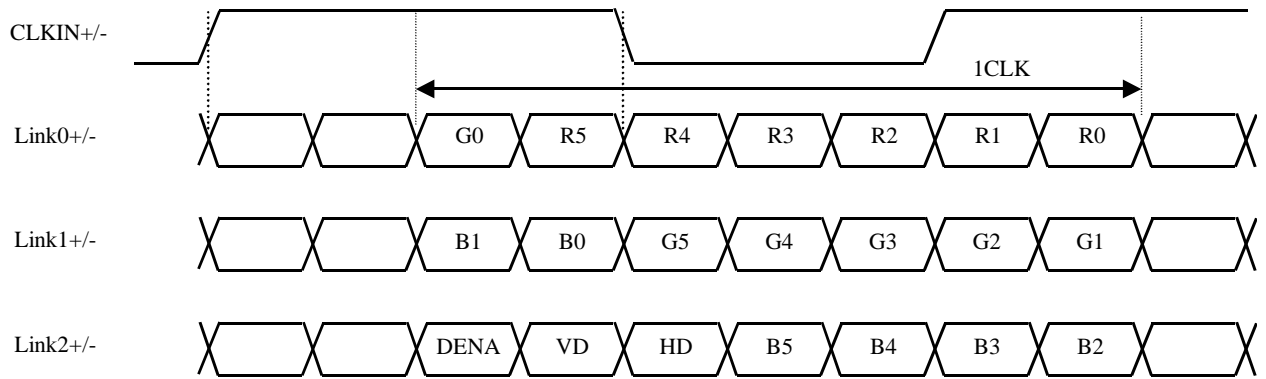
Inverter-side connector: SM04(4.0)B-BHS-1(JST)

Pin No.	Symbol	Function
1, 2	CTH	VBLH (High voltage)
4	CTL	VBLL (Low voltage)

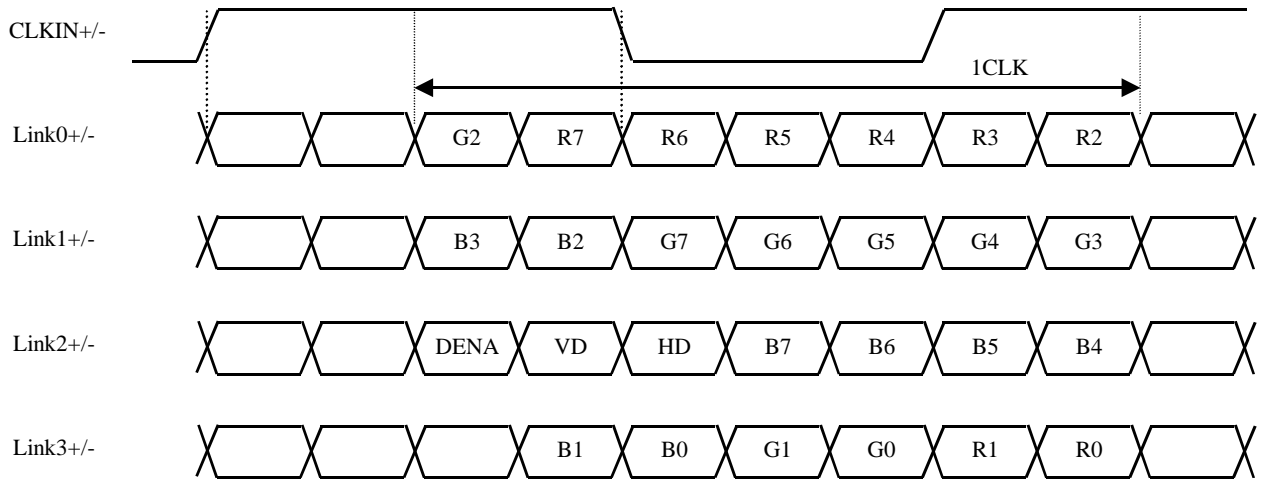
VBLH - VBLL = VL

(3) ISP data mapping

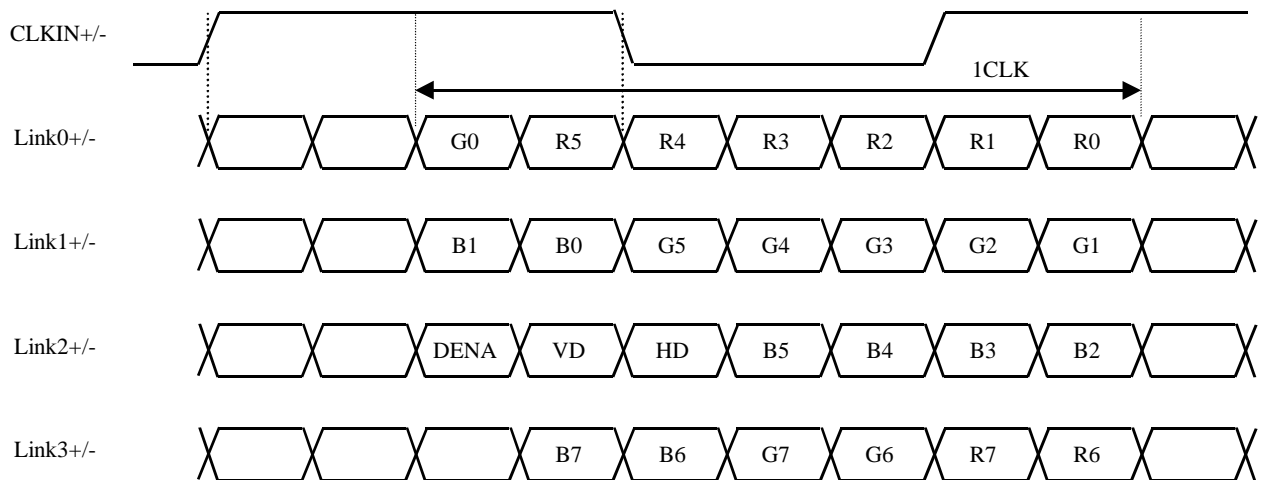
a. ISP 6 bit compatibility mode(6 bit input)



b. ISP 6 bit compatibility mode(8 bit input)



c. ISP 8 bit compatibility mode



6. INTERFACE TIMING

(1) Timing Specifications

VCC = 3.3 V, Ta = 25

ITEM		SYMBOL	MIN	TYP	MAX	UNIT
DCLK *1,4)	Frequency	f _{CLK}	35	--	40	MHz
	Period	t _{CLK}	25	--	28.6	ns
	Low Width	t _{WCL}	10	--	--	ns
	High Width	t _{WCH}	10	--	--	ns
DATA *1) (R,G,B,DENA, HD, VD)	Set up time	t _{DS}	4	--	--	ns
	Hold time	t _{DH}	4	--	--	ns
DENA *3)	Horizontal Active Time	t _{HA}	800	800	800	t _{CLK}
	Horizontal Front Porch	t _{HFP}	0	--	--	t _{CLK}
	Horizontal Back Porch	t _{HBP}	10	--	--	t _{CLK}
	Vertical Active Time	t _{VA}	600	600	600	t _H
	Vertical Front Porch	t _{VFP}	1	--	--	t _H
	Vertical Back Porch	t _{VBP}	2	--	--	t _H
HD *2,4)	Frequency	f _H	35.2	37.9	39.2	kHz
	Period	t _H	25.5	26.4	28.4	μs
	Low Width	t _{WHL}	5	--	--	t _{CLK}
VD *2)	Frequency	f _V	55	60.3	64.2	Hz
	Period	t _V	15.6	16.6	18.2	ms
	Low Width	t _{WVL}	1	--	--	t _H

[Note]

*1) DATA is latched at fall edge of DCLK in this specification.

*2) Polarities of HD and VD are negative in this specification.

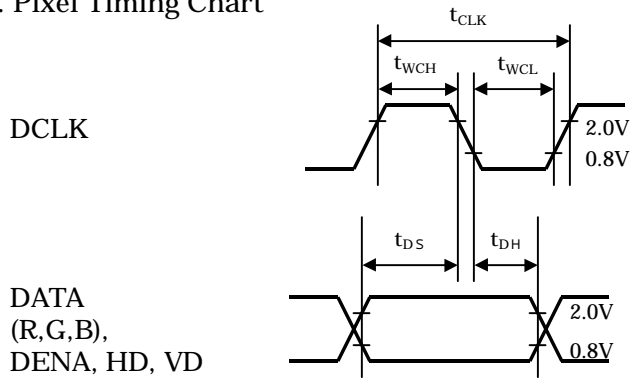
*3) DENA (Data Enable) should always be positive polarity as shown in the timing specification.

*4) DCLK should appear during all invalid period, and HD should appear during invalid period of frame cycle.

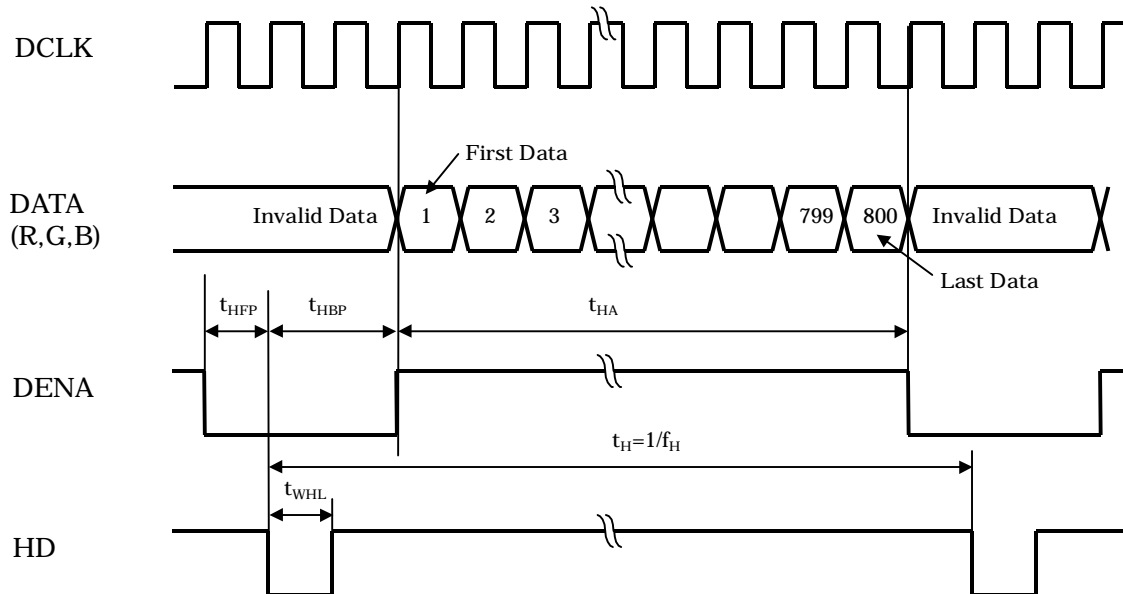
*5) LVDS timing follows the timing specifications of LVDS receiver IC: THC63LVDF84A(Thine).

(2) Timing Chart

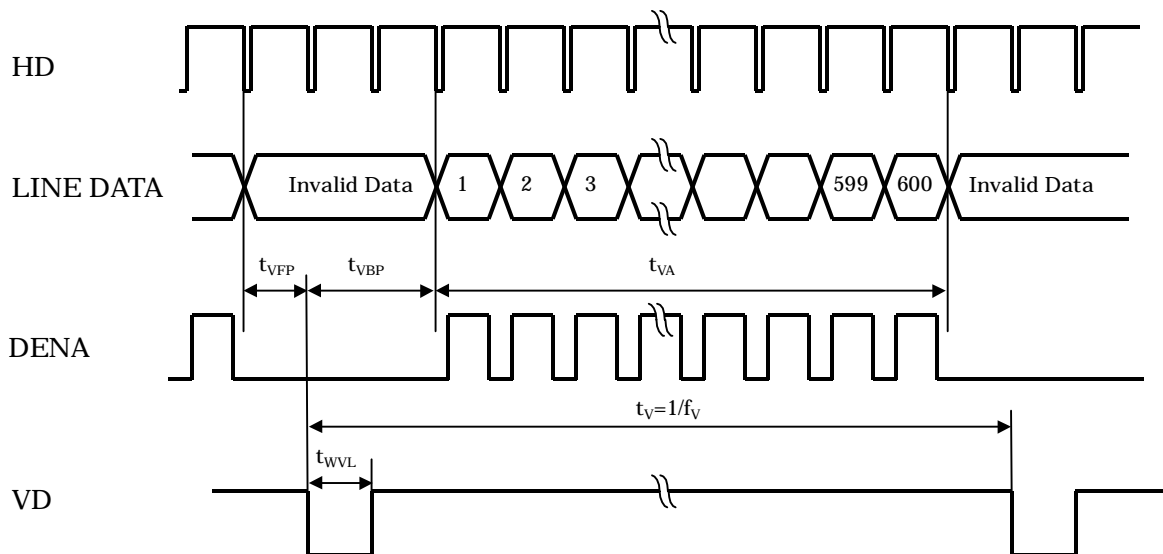
a. Pixel Timing Chart



b. Horizontal Timing Chart



c. Vertical Timing Chart



(3) Color Data Assignment

a. 6 bit input

COLOR		INPUT DATA																	
		R DATA						G DATA						B DATA					
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
		MSB					LSB	MSB					LSB	MSB					LSB
BASIC COLOR	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	CYAN	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	MAGENTA	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
RED	RED(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	RED(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
GREEN	GREEN(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	GREEN(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	GREEN(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
BLUE	BLUE(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	BLUE(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	BLUE(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	BLUE(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

[Note]

- 1) Definition of gray scale
 Color (n) --- n indicates gray scale level.
 Higher n means brighter level.
- 2) Data
 1:High, 0: Low

b. 8 bit input

COLOR		INPUT DATA																							
		R DATA								G DATA								B DATA							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
		MSB				LSB				MSB				LSB				MSB				LSB			
BASIC COLOR	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(252)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(252)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE(252)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	CYAN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	MAGENTA	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
RED	RED(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(252)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
GREEN	GREEN(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	GREEN(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	GREEN(252)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
BLUE	BLUE(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	BLUE(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	BLUE(252)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

[Note]

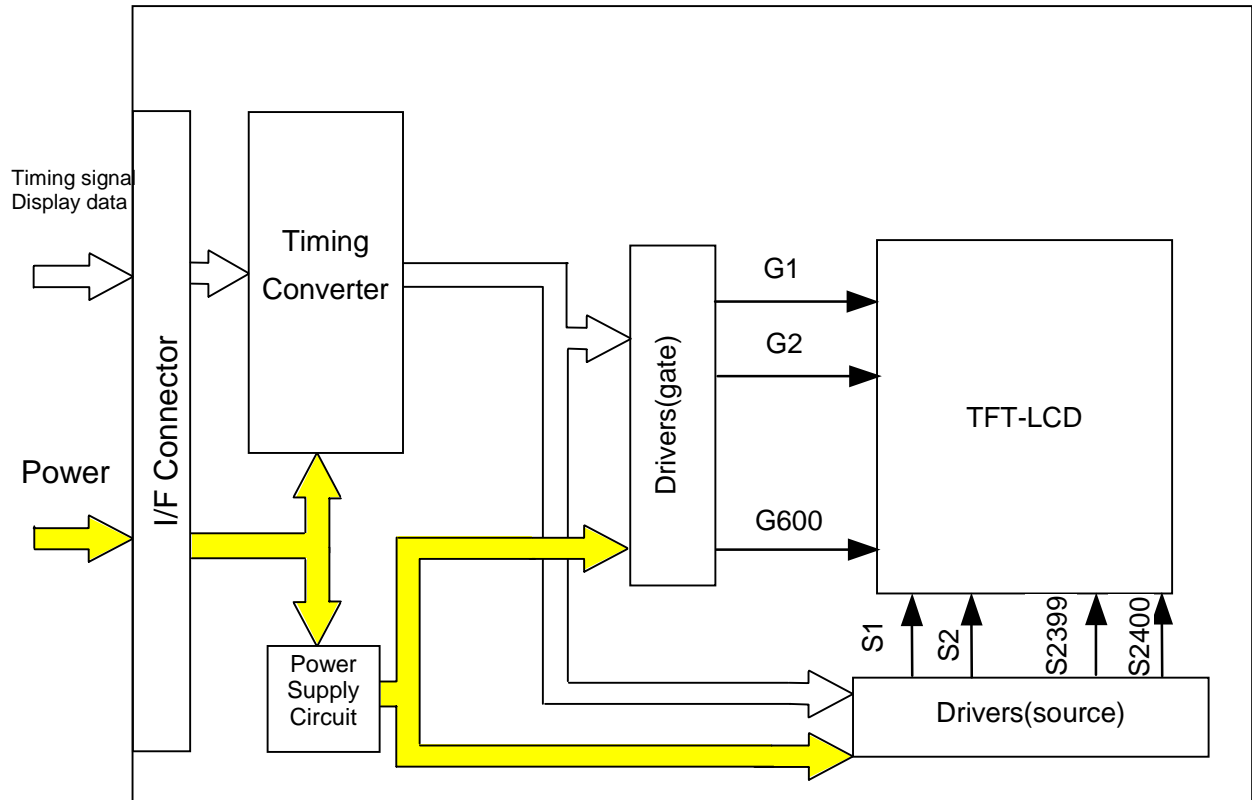
*1) Definition of gray scale
 Color (n) --- n indicates gray scale level.
 Higher n means brighter level.

*2) Data
 1:High, 0: Low

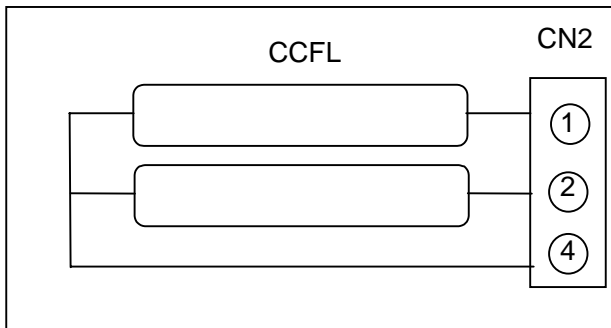
(4) Data Mapping

D(1, 1)	D(2, 1)	---	D(X, 1)	---	D(799, 1)	D(800, 1)
D(1, 2)	D(2, 2)	---	D(X, 2)	---	D(799, 2)	D(800, 2)
⋮	⋮	+	⋮	+	⋮	⋮
D(1, Y)	D(2, Y)	---	D(X, Y)	---	D(799, Y)	D(800, Y)
⋮	⋮	+	⋮	+	⋮	⋮
D(1,599)	D(2,599)	---	D(X,599)	---	D(799,599)	D(800,599)
D(1,600)	D(2,600)	---	D(X,600)	---	D(799,600)	D(800,600)

7. BLOCK DIAGRAM

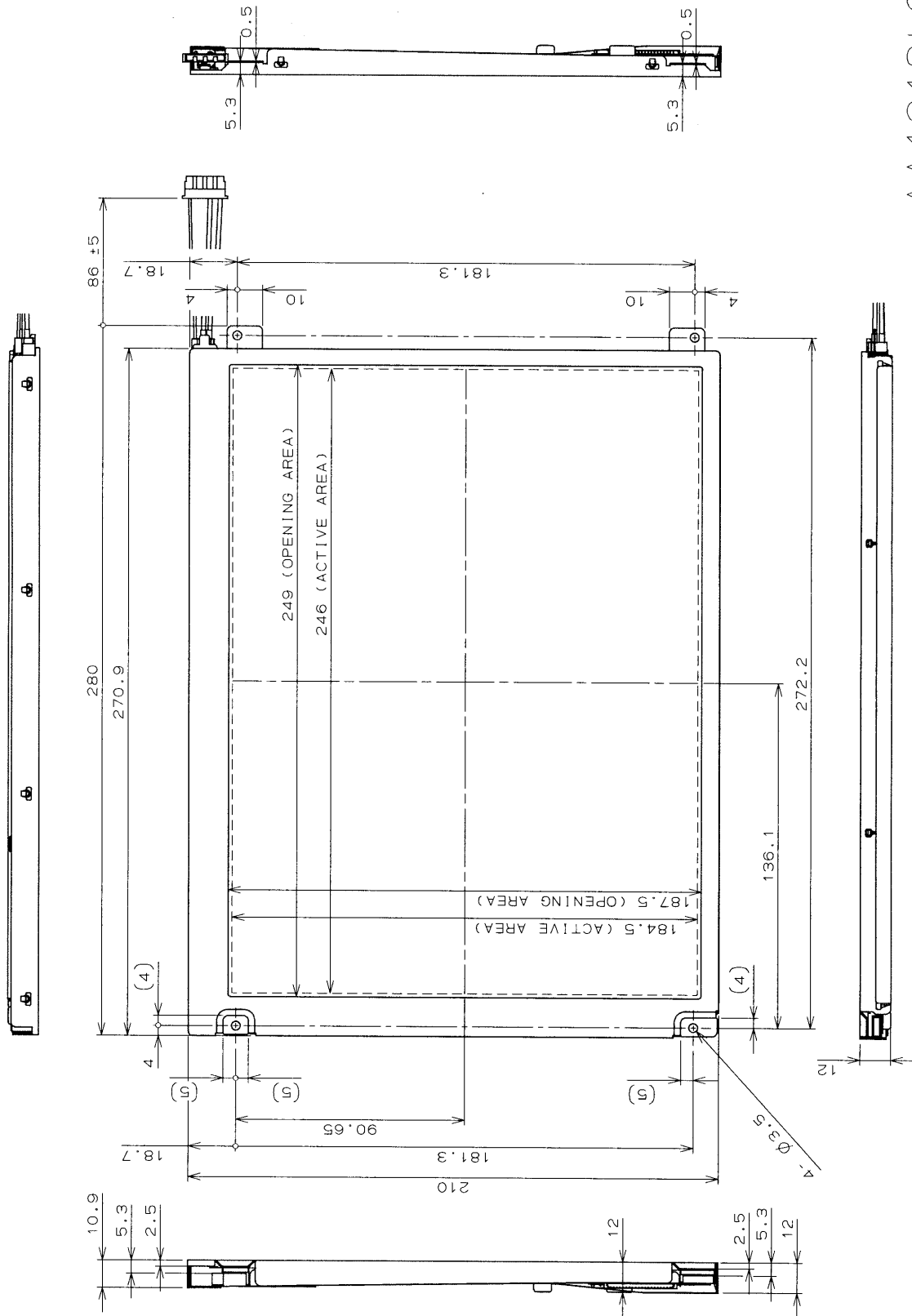


BACKLIGHT



8. MECHANICAL SPECIFICATIONS

(1) Front Side

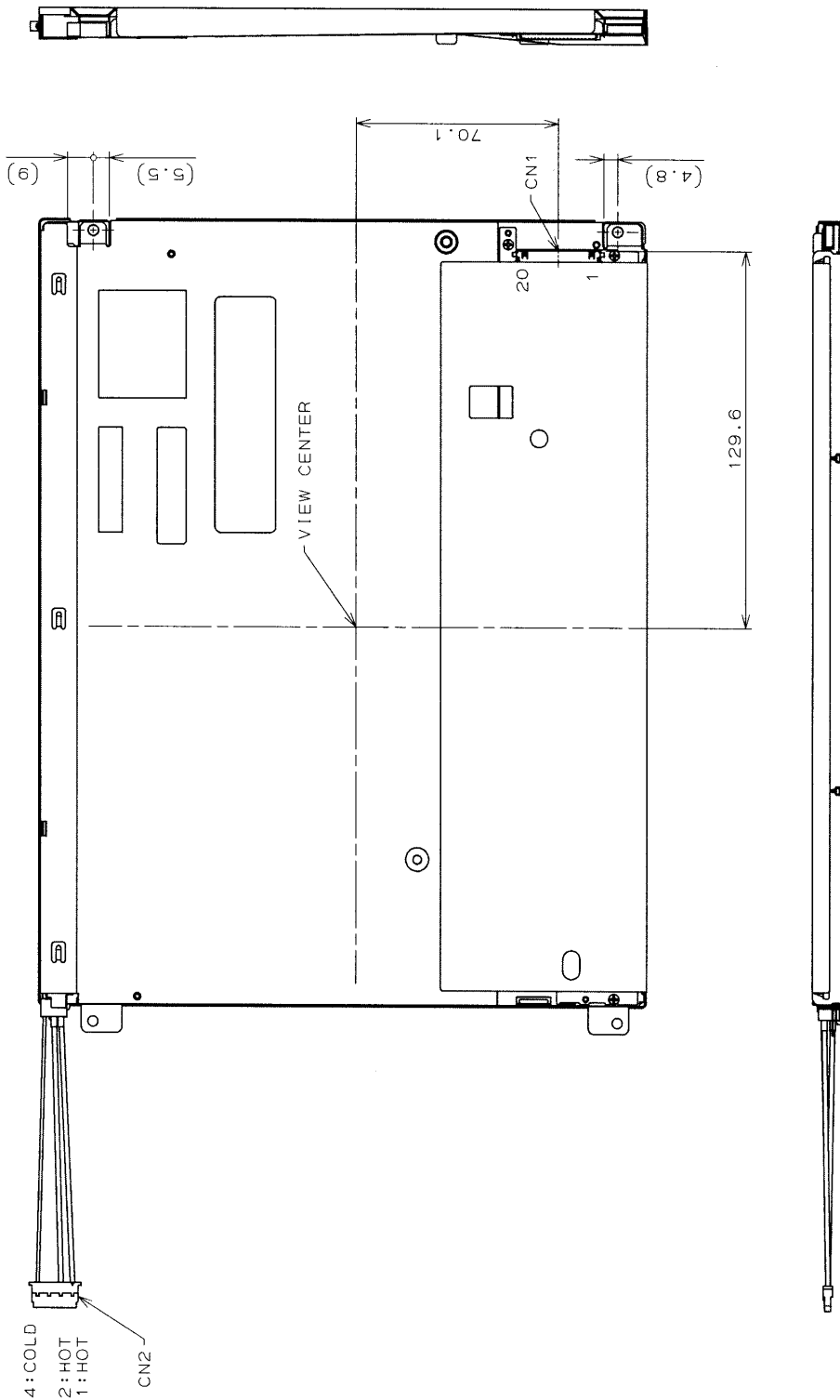


AA121SL03

UNDEFINED TOLERANCES TO BE ±0.5mm.
EXCEPT FOR THICKNESS OF PET FILM.

(Unit: mm)

(2) Rear Side



AA121SL03

UNDEFINED TOLERANCES TO BE $\pm 0.5\text{mm}$,
EXCEPT FOR THICKNESS OF PET FILM.

CN1: FI-SEB20P-HF (JAE)
CN2: BHR-04VS-1 (JST)

(Unit:mm)

[Note]

We recommend you referring to the detailed drawing for your design.
Please contact our company sales representative when you need the detailed drawing.

9. OPTICAL CHARACTERISTICS

Ta=25°C, VCC=3.3V, Input Signals: Typ. Values shown in Section 6

ITEM		SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	Remarks
Contrast Ratio		CR	$\theta = \phi = 0^\circ$	350	500	--	--	*1)*3)
Luminance		Lw	$\theta = \phi = 0^\circ$	250	350	--	cd/m ²	*2)*3)
Response Time		tr	$\theta = \phi = 0^\circ$	--	10	--	ms	*3)*4)
		tf	$\theta = \phi = 0^\circ$	--	30	--	ms	*3)*4)
Viewing Angle	Horizontal	ϕ	CR \geq 10	-50~50	-65~65	--	°	*3)
	Vertical	θ		-40~30	-75~45	--	°	*3)
Image sticking		tis	2 h	--	--	2	s	*5)
Color Coordinates	Red	Rx	$\theta = \phi = 0^\circ$	0.551	0.581	0.611	--	*3)
		Ry		0.305	0.335	0.365		
	Green	Gx		0.298	0.328	0.358		
		Gy		0.518	0.548	0.578		
	Blue	Bx		0.135	0.165	0.195		
		By		0.155	0.185	0.215		
White	Wx	0.283	0.313	0.343				
	Wy	0.299	0.329	0.359				

[Note]

These items are measured using CS1000(MINOLTA) for color coordinates, EZContrast(ELDIM) for viewing angle and CS1000 or BM-5A(TOPCON) for others under the dark room condition (no ambient light) after more than 30 minutes from turning on the lamp unless noted.

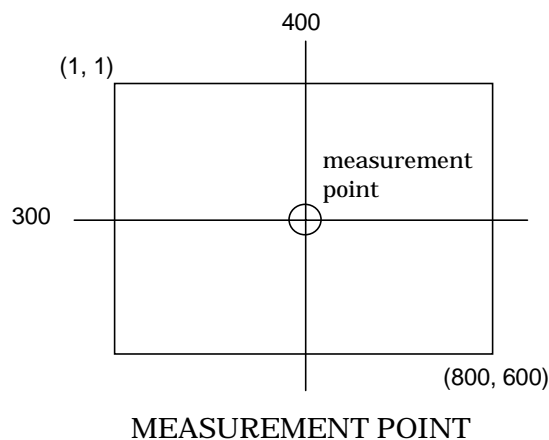
Condition: IL = 12.0 mArms, FL=55kHz

*1) Definition of Contrast Ratio

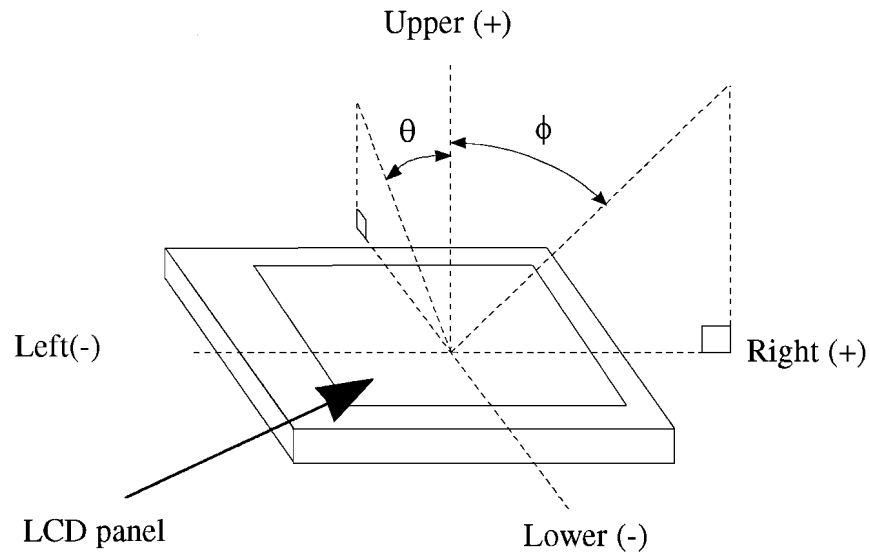
CR = ON (White) Luminance / OFF (Black) Luminance: measurement point shown in a figure below

*2) Definition of Luminance

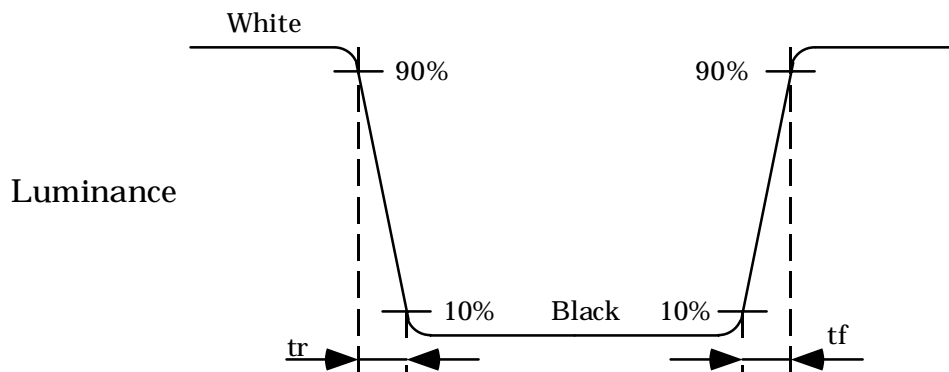
Lw= ON (White) Luminance: measurement point shown in a figure below



*3) Definition of Viewing Angle(θ , ϕ)



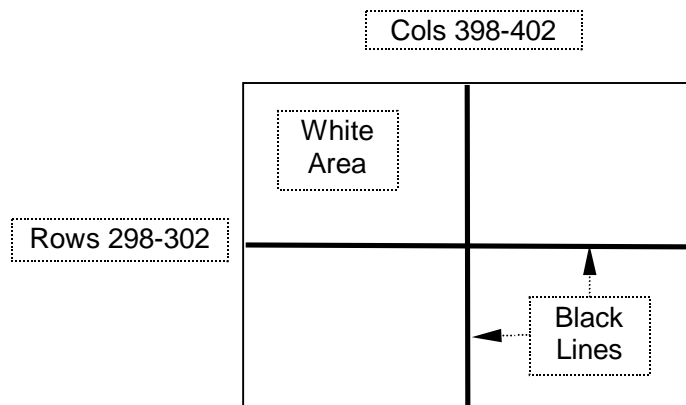
*4) Definition of Response Time



*5) Image sticking:

Continuously display the test pattern shown in the figure below for two-hours. Then display a completely white screen. The previous image shall not persist more than two seconds at 25°C.

TEST PATTERN FOR IMAGE STICKING TEST



10. RELIABILITY TEST CONDITION

(1) Temperature and Humidity

TEST ITEM	CONDITIONS
HIGH TEMPERATURE HIGH HUMIDITY OPERATION	40°C, 90%RH, 240 h
HIGH TEMPERATURE STORAGE	70°C, 96 h
LOW TEMPERATURE STORAGE	-20°C, 96 h
THERMAL SHOCK	BETWEEN -20°C (1h) and 70°C(1h), 100 CYCLES

(2) Shock & Vibration

ITEM	CONDITIONS
SHOCK (NON-OPERATION)	Shock level: 1470m/s ² (150G) Waveform: half sinusoidal wave, 2ms Number of shocks: one shock input in each direction of three mutually perpendicular axes for a total of six shock inputs
VIBRATION (NON-OPERATION)	Vibration level: 9.8m/s ² (1.0G) Waveform: sinusoidal Frequency range: 5 to 500Hz Frequency sweep rate: 0.5 octave /min Duration: one sweep from 5 to 500 Hz in each of three mutually perpendicular axis(each x,y,z axis: 1 hour, total 3 hours)

(3) Judgment standard

The judgment of the above tests should be made as follow:

Pass: Normal display image with no obvious non-uniformity and no line defect.

Partial transformation of the module parts should be ignored.

Fail: No display image, or line defects.

11. HANDLING PRECAUTIONS FOR TFT-LCD MODULE

Please pay attention to the followings in handling TFT-LCD products;

(1) ASSEMBLY PRECAUTION

- a. Please use the mounting hole on the module in installing and do not bending or wrenching LCD in assembling. And please do not drop, bend or twist LCD module in handling.
- b. Please design display housing in accordance with the following guide lines.
 - (a) Housing case must be designed carefully so as not to put stresses on LCD all sides and not to wrench module. The stresses may cause non-uniformity even if there is no non-uniformity statically.
 - (b) Keep sufficient clearance between LCD module back surface and housing when the LCD module is mounted. Approximately 1.0mm of the clearance in the design is recommended taking into account the tolerance of LCD module thickness and mounting structure height on the housing.
 - (c) When some parts, such as, FPC cable and ferrite plate, are installed underneath the LCD module, still sufficient clearance is required, such as 0.5mm. This clearance is, especially, to be reconsidered when the additional parts are implemented for EMI countermeasure.
 - (d) Design the inverter location and connector position carefully so as not to give stress to lamp cable, or not to interface the LCD module by the lamp cable.
 - (e) Keep sufficient clearance between LCD module and the others parts, such as inverter and speaker so as not to interface the LCD module. Approximately 1.0mm of the clearance in the design is recommended.
- c. Please do not push or scratch LCD panel surface with anything hard. And do not soil LCD panel surface by touching with bare hands. (Polarizer film, surface of LCD panel is easy to be flawed.)
- d. Please do not press any parts on the rear side such as source TCP, gate TCP, control circuit board and FPCs during handling LCD module. If pressing rear part is unavoidable, handle the LCD module with care not to damage them.
- e. Please wipe off LCD panel surface with absorbent cotton or soft cloth in case of it being soiled.
- f. Please wipe off drops of adhesives like saliva and water on LCD panel surface immediately. They might damage to cause panel surface variation and color change.
- g. Please do not take a LCD module to pieces and reconstruct it. Resolving and reconstructing modules may cause them not to work well.
- h. Please do not touch metal frames with bare hands and soiled gloves. A color change of the metal frames can happen during a long preservation of soiled LCD modules.
- i. Please handle metal frame carefully to avoid getting hurt because edge of metal frame is very sharp.

- j. Please pay attention to handling lead wire of backlight so that it is not tugged in connecting with inverter.
- k. Be sure to connect the cables and the connectors correctly.
- l. Please connect the metal frame of LCD module to GND in order to minimize the effect of external noise and EMI.

(2) OPERATING PRECAUTIONS

- a. Please be sure to turn off the power supply before connecting and disconnecting signal input cable.
- b. Please do not change variable resistance settings in LCD module. They are adjusted to the most suitable value. If they are changed, it might happen LCD does not satisfy the characteristics specification.
- c. LCD backlight takes longer time to become stable of radiation characteristics in low temperature than in room temperature.
- d. The interface signal speed is very high. Please pay attention to transmission line design and other high speed signal precautions to satisfy signal specification.
- e. A condensation might happen on the surface and inside of LCD module in case of sudden change of ambient temperature.
- f. Please pay attention not to display the same pattern for very long time. Image might stick on LCD. Even if image sticking happens, it may disappear as the operation time proceeds.
- g. Please obey the same safe instructions as ones being prepared for ordinary electronic products.

(3) PRECAUTIONS WITH ELECTROSTATICS

- a. This LCD module use CMOS-IC on circuit board and TFT-LCD panel, and so it is easy to be affected by electrostatics. Please be careful with electrostatics by the way of your body connecting to the ground and so on.
- b. Please remove protection film very slowly from the surface of LCD module to prevent from electrostatics occurrence.

(4) STORAGE PRECAUTIONS

- a. Please do not leave the LCDs in the environment of high humidity and high temperature such as 60°C90%RH.
- b. Please do not leave the LCDs in the environment of low temperature; below -20°C.

(5) SAFETY PRECAUTIONS

- a. When you waste damaged or unnecessary LCDs, it is recommended to crush LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
- b. If any liquid leaks out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.
- c. Be sure to turn off the power supply when inserting or disconnecting the cable.
- d. Inverter should be designed carefully so as not to keep working in case of detecting over current or open circuit on the lamp.

(6) OTHERS

- a. A strong incident light into LCD panel might cause display characteristics changing inferior because of polarizer film, color filter, and other materials becoming inferior. Please do not expose LCD module direct sunlight and strong UV rays.
- b. Please pay attention to a panel side of LCD module not to contact with other materials in preserving it alone.
- c. For the packaging box, please pay attention to the followings;
 - (a) Packaging box and inner case for LCD are designed to protect the LCDs from the damage or scratching during transportation. Please do not open except picking LCDs up from the box.
 - (b) Please do not pile them up more than 5 boxes. (They are not designed so.) And please do not turn over.
 - (c) Please handle packaging box with care not to give them sudden shock and vibrations. And also please do not throw them up.
 - (d) Packaging box and inner case for LCDs are made of cardboard. So please pay attention not to get them wet. (Such like keeping them in high humidity or wet place can occur getting them wet.)